4/IDS -1/26/99 Dm

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Hiroyuki WATANABE et al.

Serial Number: Not Yet Assigned

Filed: May 27, 1999

For: SEMICONDUCTOR DEVICE AND FABRICATION METHOD THEREOF

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents Washington, D.C. 20231

May 27, 1999

Sir:

In compliance with 37 CFR 1.56, Applicants call to the attention of the Patent and Trademark Office the references listed on the attached PTO-1449.

A copy of each of the references is enclosed herewith.

In the event there are any fees due in connection with the filing of this paper, please charge Deposit Account No. 01-2340.

Respectfully submitted,
ARMSTRONG, WESTERMAN, HATTORI
McLELAND & NAUGHTON

Stephen G. Adrian

Reg. No. 32,878

Atty. Docket No.: 990559

Suite 1000, 1725 K Street, N.W.

Washington, D.C. 20006

Tel: (202) 659-2930

Fax: (202) 887-0357

SGA/tmb

Enclosures: PTO-1449; References (4).